#### 1. A dual issue queued pipelined Java processor TRAJA-toward an open source processor project

Shimizu, N.; Naito, M.;

ASIGs, 1999. AP ASIG '99. The First IEEE Asia Pacific Conference on

23-25 Aug. 1999 Page(s):213 - 216

Digital Object Identifier 10.1109/APASIC.1999.824066

AbstractPlus I Full Text: PDF(264 KB) EEEE CMG Rights and Permissions

2. H. 264 Decoder at HD Resolution on a Coarse Grain Dynamically Reconfigurable Architecture

Ganesan, M.K.A.; Singh, S.; May, F.; Becker, J.;

Field Programmable Logic and Applications, 2007. FPL 2007. International Conference on

27-29 Aug. 2007 Page(s):467 - 471

Digital Object Identifier 10.1109/FPL.2007.4380691

AbstractPlus | Full Text: PDF(2691 KB) SEEE CNF

Rights and Permissions

### 3. A four-processor building block for SIMD processor arrays

Fisher, A.L.; Highnam, P.T.; Rockoff, T.E.;

Sold-State Circuits, IEEE Journal of Volume 25, Issue 2, April 1990 Page(s):369 - 375

Digital Object Identifier 10.1109/4.52158

AbstractPius | Full Text: PDF(760 KB) #EEE JNE

Rights and Permissions

# 4. A mainframe processor in CMOS technology with 0.5 µm channel length

Schettler, H.; Hajdu, J.; Getzlaff, K.; Loehlein, W.-D.; Starke, C.-W.;

Sclid-State Circuits Conference, 1990. Digest of Technical Papers. 37th ISSCC., 1990 IEEE International 14-16 Feb. 1990 Page(s):50 - 51

Digital Object Identifier 10.1109/ISSCC.1990.110124

AbstractPlus | Full Text: PDF(276 KB) RESEGNE

Rights and Permissions

#### 5. The architecture of a processor array for video decompression

Mayer, A.C.;

Consumer Electronics, IEEE Transactions on

Volume 39, Issue J, Aug. 1993 Page(s):565 - 569

Digital Object Identifier 10.1109/30.234636

AbstractPlus | Full Text: PDF(328 KB) | RESELING

Rights and Permissions

## 6. Design of Digital Audio DSP Core

Chang Won Ryu; Dong Hun Lee; Hua Jun Chi; Kyoung Su Kwan; Tae Hoon Kim; Ju Sung Park;

Strategic Technology, The 1st International Forum on 18-20 Oct. 2006 Page(s):59 - 62

Digital Object Identifier 10.1109/IFOST.2006.312246

AbstractPlus | Full Text: PDF(3842 KB) REEE CONF

**Bights and Permissions** 

### 7. HW/SW codesign of the MPEG-2 video decoder

Verderber, M.; Zemva, A.; Trost, A.;

Parallei and Distributed Processing Symposium, 2003, Proceedings, International

22-26 April 2003 Page(s):7 pp.

Digital Object Identifier 10.1109/IPDPS.2003.1213330

AbstractPlus | Full Text: PDE(1347 KB) ■ EEE C&F

Rights and Permissions

	8.	Polymorphic processors: how to expose arbitrary hardware functionality to programmers?  Vassilladis, S.;
		Parallel Architecture and Compilation Techniques, 2004. PACT 2004. Proceedings, 13th International Conference on 29 Sept3 Oct. 2004 Page(s):239
		Digital Object Identifier 10.1109/PACT.2004.1342557
		AbstractPlus   Full Text: POF (230 KB) XEEE CAP Flights and Permissions
	9.	In-system FPGA prototyping of an Itanium microarchitecture Wunderlich, R.E.; Hoe, J.C.;
		Compatier Design: VLSI in Computers and Processors, 2004, ICCD 2004, Proceedings, IEEE Infernational Conference on 11-13 Oct. 2004 Page(s): 288 - 294 Digital Object (dentifier 10.1109)ICCD 2004.1347935
		AbstractPlus   Full Text: PDE(471 KB) REE ONE Bighte and Permissions
	10.	Dynamic addressing memory arrays with physical locality Heu, S.; Shih-Llen Lu; Shih-Chang Lai; Krishnamurthy, R.; Lai, K.;
		Microarchitecture, 2002. (MICRO-35). Proceedings, 35th Annual IEEE/ACM International Symposium on
		18-22 Nov. 2002 Page(s):161 - 170
		Digital Object Identifier 10.1109/MICRO.2002.1176247
		AbstractPlus   Full Text: PDF(312 KB)   NIEE CNF   Rights and Permessions
	11.	Custom processor design using NISC: a case-study on DCT algorithm Gorjiara, B.; Gajaki, D.;
		Embedded Systems for Fieat-Time Multimedia, 2005, 3rd Workshop on
		22-23 Sept. 2005 Page(s):55 - 60
		Digital Object Identifier 10.1109/ESTMED.2005.1518072
		AbstractPus   Full Text: PDE(461 KB)   ISEE CRIF Ragins and Permessions
	12.	The NSR processor Brunvand, E.;
		System Sciences, 1993, Proceeding of the Twenty-Sidth Hewaii International Conference on Volume I, 5-8 Jan 1993 Page(e):428 - 435 vol.1
		Digital Object Identifier 10.1109/HICSS.1993.270622
		AbstractPus   Full Text: PDF (723 KB) HEEL CNF Bignts, and Permissions
	13.	Determining the optimum extended instruction-set architecture for application specific reconfigurable VLIW CPUs Alippi, C.; Fornaciari, W.; Pozzi, L.; Sami, M.;
		Rapid System Prototyping, 12th International Workshop on, 2001,
		25-27 June 2001 Page(s):50 - 56
		Digital Object Identifier 10.1109/IWRSP.2001.933838
		AbstractCus   Full Text_PDF (598 KB)   IEEE CNF Bighte, and Permissions
***************************************	14.	Implementation of RISC Processor on FPGA Mane, P.S.; Gupta, I.; Vasantha, M.K.;
		Industrial Technology, 2006, IC1T 2006, IEEE international Conference on
		15-17 Dec. 2006 Page(s):2096 - 2100
		Digital Object Identifier 10.1109/ICIT.2006.372448
		AbstractPlus   Full Text: PDF (2451 KB) SEEF CNF Rights and Permessions
П	15.	Implementation of H.264/AVC decoder for mobile video applications Suh Ho Lee, Ji Hwan Park; Seon Wook Kim; Sung Jea Ko; Suki Kim;
		Design Automation, 2008. Asia and South Pacific Conference on
		24-27 Jan 2006 Page(s):2 pp.

Digital Object Identifier 10.1109/ASPDAC.2006.1594666

AbstractPlus | Full Text: PDF(328 KB) IEEE CNF Rights and Permissions 16. Entropy Coding on a Programmable Processor Array for Multimedia SoC Osorio, R.R.; Bruguera, J.D.; Application - specific Systems, Architectures and Processors, 2007. ASAP. IEEE International Conf. on 9-11 July 2007 Page(s):222 - 227 Digital Object Identifier 10.1109/ASAP.2007.4429984 AbstractPlus | Full Text: PDF (257 KB) | SEEF CNF Rights and Permesions 17, Implementation of H.264/AVC decoder for mobile video applications Suh Ho Lee; Jeong Hun Kim; Ji Hwan Park; Seon Wook Kim; Suki Kim; Circuits and Systems, 2006. ISCAS 2006. Proceedings, 2006 IEEE International Symposium on 21-24 May 2006 Page(s):4 pp. Digital Object Identifier 10.1109/ISCAS.2006.1693901 AbstractPlus | Full Text: PDF(1440 KB) #EEE CRE Rights and Permissions 18. COS/MOS parallel processor array Alaspa, A.; Dingwall, A.; Solid-State Circuits Conference, Digest of Technical Papers, 1970 IEEE International Volume XIII, Feb 1970 Page(s):118 - 119 AbstractPlus | Full Text: PDF(448 KB) | SEEF CNF Rights and Permesions 19. A 650-MHz, IA-32 microprocessor with enhanced data streaming for graphics and video Senthinathan, R.; Fischer, S.; Rangchi, H.; Yazdanmehr, H.; Solid-State Circuits, IEEE Journal of Volume 34, Issue 11, Nov. 1999 Page(s):1454 - 1465 Digital Object Identifier 10.1109/4.799850 AbstractPlus | References | Full Text: PDF(1096 KB) KEEE JNL Rights and Permissions 20. A design of 500MHz 10-read 6-write register file Yu Qian; Wang Dong-hui; Zhang Tie-jun; Hou Chao-huan; ASIC, 2005. ASICON 2005. Bth International Conference On Volume 1, 24-27 Oct. 2005 Page(s):311 - 315 Digital Object Identifier 10.1109/ICASIC.2005.1611301 AbstractPlus | Full Text: PDF(1504 KB) SEEE CNF Righte and Permissions 21. Low-energy heterogeneous digit-serial Reed-Solomon codecs Song, L.; Parhi, K K.; Kuroda, I.; Nishitani, T.; Acoustics, Speech and Signal Processing, 1998. Proceedings of the 1998 IEEE International Conference on Volume 5, 12-15 May 1998 Page(s):3049 - 3052 vol.5 Digital Object Identifier 10.1109/ICASSP.1998.678169 AbstractPlus | Full Text: PDF(456 KB) IEEE CNF Rights and Permissions 22. The design of a linear image processor Fung, Y.-F.; Fountain, T.J.; TENCON '92, Technology Enabling Tomorrow: Computers, Communications and Automation towards the 21st Century, 1992 IEEE Region 10 International Conference. 11-13 Nov. 1992 Page(s):998 - 1002 vol.2 Digital Object Identifier 10.1109/TENCON.1992.272039 AbstractPlus | Full Text: PDF(312 KB) #EEE CNF Righte and Permissions

23.	ARM compatible 32-bit RISC processor design and verification Genuryoung Lengu-Ju-sump Park I; hymowo Los Byung-yoo Yoon, Myung-jin Lee; Soenor and Lectnology, 2005. KORUS 2005. Proceedings. The 9th Russian-Korean International Symposium 26 June 2. July 2005 Page(s) 507 - 610 Digital Cloyed Leitherfier 10.1 108/KORUS 2005. 1507795
	AbstractPlus   Full Text: (PQF (226 KB) sEEE CNF- Rignss and Permissions
24.	Design of 32-bit RISC processor and efficient verification Geun-young Jeong; Ju-sung Park;
	Science and Technology, 2003. Proceedings KORUS 2003. The 7th Korea-Flussis International Symposium on Volume 2, 28 June-6 July 2003 Page(s):222 - 227 vol.2
	Digital Object Identifier 10.1109/KORUS.2003.1222609
	AbstractPlus   Full Text: PDF (379 KB)   IE EE CNF Flights and Permissions
25.	Optoelectronic took-up table using VCSEL-based logic Mitkas, P.A.; Irakliotis, L.J.; Beyette, F.R., Jr.; Feld, S.A.; Wilmsen, C.W.;
	Lasers and Electro-Optics Society Annual Meeting, 1993, LEOS '93 Conference Proceedings, IEEE
	15-18 Nov. 1993 Page(s):71 - 72
	Digital Object Identifier 10.1109/LEOS.1993.379121
	24.

AbstractPlus | Full Text: PDF(144 KB) NEEE CNF